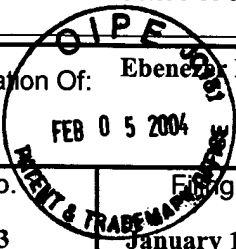


TRANSMITTAL OF FORMAL DRAWINGSDocket No.
BUR920030058US1 (16693)In Re Application Of: **Ebenezer Eshun, et al.**
FEB 05 2004Serial No.
10/707,863Filing Date
January 19, 2004Confirmation No.
UnknownExaminer
UnknownArt Unit
UnknownInvention: **HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR AND RF SiGe BiCMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE AND ESD RESISTOR BALLASTING FEATURE**

Address to:

**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

Transmitted herewith are:

11 sheets of formal drawing(s) for this application.☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).*Signature***Steven Fischman
Registration No. 34,594****Correspondence Address
Customer No. 23389**Dated: **February 2, 2004**I certify that this document and attached formal drawings are being deposited on **February 2, 2004** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.*Signature of Person Mailing Correspondence***Steven Fischman***Typed or Printed Name of Person Mailing Correspondence*